

October 29, 1996

PCN-964501

Product Change Notification

x32 Synchronous Burst SRAM Design Upgrade

Products affected: AS7C36432, AS7C33232F

Description of Change:

Single mask change to improve yield to 100 MHz.

Details:

The designs of the AS7C36432 and AS7C33232F were planned to support speeds to 117 MHz for Pentium Pro applications. Initial speed of these products, though, was targeted at 66-75 MHz. A single mask change was made to improve yield to 100 MHz to accommodate future upgrades to 117 MHz. This change was tested in a variety of motherboards across temperature to insure continued compatibility with common chipsets and CPU's.

Schedule:

Production quantities of the changed AS7C36432 are shipping today. Production quantities of the AS7C33232F will be shipping by mid-December.

Disposition:

There are no changes to DC or AC performance. Alliance does not view this as a major (form, fit, or function) change which might require requalification.

Questions regarding this Product Change Notification should be forwarded to a local Alliance sales representative.

Regards,

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