



8/15/96

PCN-963301\*\*

## 32K×32 SRAM Product Change Notification

### **Change Description: New features, supports high speed CPUs**

This letter is to serve as a notification of a product change for Alliance Semiconductor part number AS7C33232. The new 32K×32 product revision is currently sampling and applies to all speeds and packages. It is designed to meet or exceed the specifications set forth in the AS7C33232 data sheet and is now listed as part number AS7C33232F. The new revision is produced using 0.4μ process technology, a step up from 0.5μ technology used to produce the previous revision.

Highlights of the new 32K×32 SRAM revision:

- 0.4μ technology and 2 metal, 2 poly process.
- Supports bus speeds above 100 MHz.
- Includes flow-through functionality option and ZZ 'sleep mode.'
- Supports next generation CPU's including Cyrix P200+ and Pentium-166 and -200.
- $t_{CD}$  improvement to 7ns or better at 66 MHz

Alliance has made this revision in order to deliver the most competitive performance and cost effective solution to our customers at the highest levels of quality. Shipments of samples of this revision will commence immediately with volume shipments following in September.

Please contact your local sales representative if you have any questions regarding this information.

Sincerely,

Anwar Khan  
Quality Director

Sid Agrawal  
Vice President, Marketing

***ALLIANCE SEMICONDUCTOR***  
3099 NORTH FIRST STREET SAN JOSE, CA 95134